

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant :	Koichiro Tanaka	Art Unit :	2822
Serial No. :	09/842,797	Examiner :	Michael Manh Trinh
Filed :	April 27, 2001	Conf. No. :	7383
Title :	METHOD OF FABRICATING SEMICONDUCTOR DEVICE		

MAIL STOP RCE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Please consider the references listed on the enclosed PTO-1449 form. Cited U.S. patents and patent application publications will be provided on request.

This filing is being made with the filing of a Request for Continued Examination. No fee is required.

Respectfully submitted,

Date: March 5, 2009



John F. Hayden
Reg. No. 37640

Customer No. 26171
Fish & Richardson P.C.
1425 K Street, N.W., 11th Floor
Washington, DC 20005-3500
Telephone: (202) 783-5070
Facsimile: (877) 769-7945